

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3549	((address near4 transit\$3 near4 detect\$3) ATD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L2	0	((sense adj2 (amp amplifier)) with (reset adj3 signal)) and ((control\$5 adj3 circuit) with set\$4 with latch) and ((control\$5 adj3 circuit) with reset\$4 with latch)) and (((address near4 transit\$3 near4 detect\$3) ATD))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L3	30	((sense adj2 (amp amplifier)) with (reset adj3 signal)) and ((control\$5 adj3 circuit) with set\$4 with latch) and ((control\$5 adj3 circuit) with reset\$4 with latch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L4	148	((control\$5 adj3 circuit) with (set\$4 adj4 latch)) and ((control\$5 adj3 circuit) with (reset\$4 adj4 latch))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L5	13	((address near4 transit\$3 near4 detect\$3) ATD)) and (((control\$5 adj3 circuit) with (set\$4 adj4 latch)) and ((control\$5 adj3 circuit) with (reset\$4 adj4 latch)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L6	5545	((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L7	40	((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch))) and ((sense adj2 (amp amplifier)) with generat\$3 with (reset adj3 signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L8	9	control\$4 same ((latch (flip adj flop)) with set) same ((latch (flip adj flop)) with reset) same ((latch (flip adj flop)) with (metastable (meta adj stable)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L9	12	((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch))) and (output with (metastab\$5 (meta adj stab\$5)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30

L10	4793	(latch (flip adj flop)) with set with reset with (active high hi asserted on)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L11	435	(latch (flip adj flop)) with (set near3 reset) adj4 (active high hi asserted on)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L12	435	(latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L13	0	((latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on))) same (metastab\$5 (meta adj stab\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L14	6	((latch (flip adj flop)) with ((set near3 reset) adj4 (active high hi asserted on))) and (metastab\$5 (meta adj stab\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L15	1736	(latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L16	28	((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L17	21	((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L18	7	((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2. reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30

L19	21	((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L20	397	(sense adj (amp amplifier)) with (reset adj3 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L21	16	((sense adj (amp amplifier)) with (reset adj3 signal)) with (read\$3 with cell)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L22	145	(sense adj (amp amplifier)) with generat\$3 with (reset adj3 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L23	26	(sense adj (amp amplifier)) near5 generat\$3 near5 (reset adj3 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L24	11	(sense adj (amp amplifier)) adj5 generat\$3 adj5 (reset)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L25	92	(sense adj (amp amplifier)) adj5 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L26	68	(sense adj (amp amplifier)) adj3 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L27	27	(sense adj (amp amplifier)) with (reset adj2 signal) with (read\$3 adj2 operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30

L28	9	(sense adj (amp amplifier) adj3 reset adj3 signal) with (controller (control adj (circuit unit)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L29	15	(sense adj3 (amp amplifier)) near3 generat\$3 near3 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/02 21:30
L30	15	(sense adj3 (amp amplifier)) near3 generat\$3 near3 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L31	68	(sense adj (amp amplifier)) adj3 (reset adj2 signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L32	40	((((control\$5 adj3 circuit) with (set reset) with ((flip adj flop) latch))) and ((sense adj2 (amp amplifier)) with generat\$3 with (reset adj3 signal)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L33	7	L30 and L31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L34	3	L31 and L32	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L35	344	(sense adj3 (amp amplifier)) with reset with (latch (flip adj flop))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L36	21	((((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) not (((latch (flip adj flop)) with ((set near2 reset) with (active high hi assert\$3 on))) and (metastab\$5 (meta adj stab\$5))) and memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30

L37	0	L35 and L36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L38	0	L31 and L36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L39	98	(sense adj3 (amp amplifier)) near3 reset near3 (latch (flip adj flop))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L40	219	(sense adj3 (amp amplifier)) same reset same (latch (flip adj flop)) same set same read\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L41	1	((sense adj3 (amp amplifier)) adj5 reset adj5 (latch (flip adj flop))) with (read\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30
L42	2	"5740111".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/02 21:30